

A Formal Semantics for PLEX

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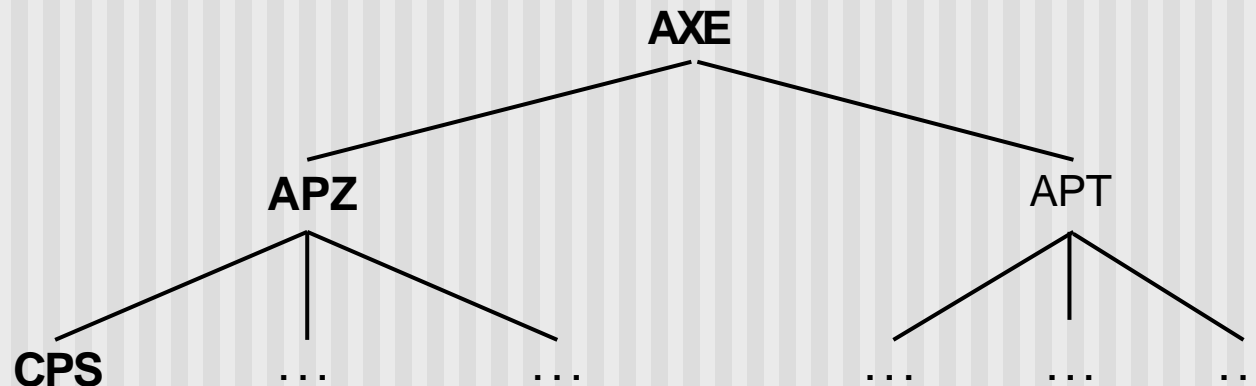
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A Formal Semantics for PLEX

The AXE telephone exchange system

- 1970 – Earliest version
- PLEX used in central parts of the AXE system
- ~ 10 mil. lines of code



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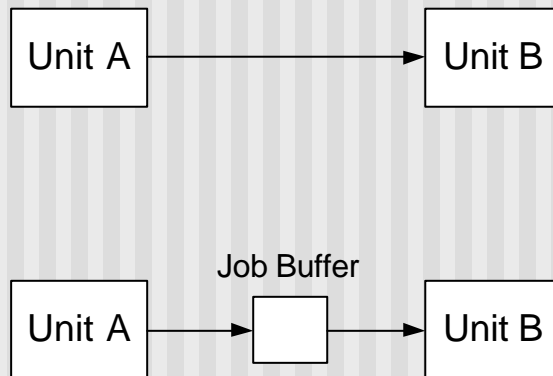
PLEX characteristics

- Single-purpose: Exclusively for telephony systems (*Programming Language for EXchanges*)
- Event-based with a signal paradigm as its top execution level.
- Sequential but "pseudo-parallel"
- A parallel domain!

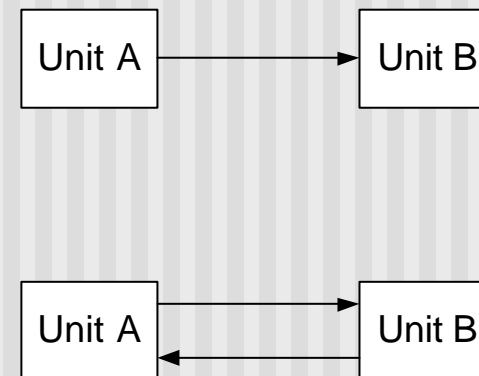
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PLEX characteristics - signals

Direct vs. Buffered



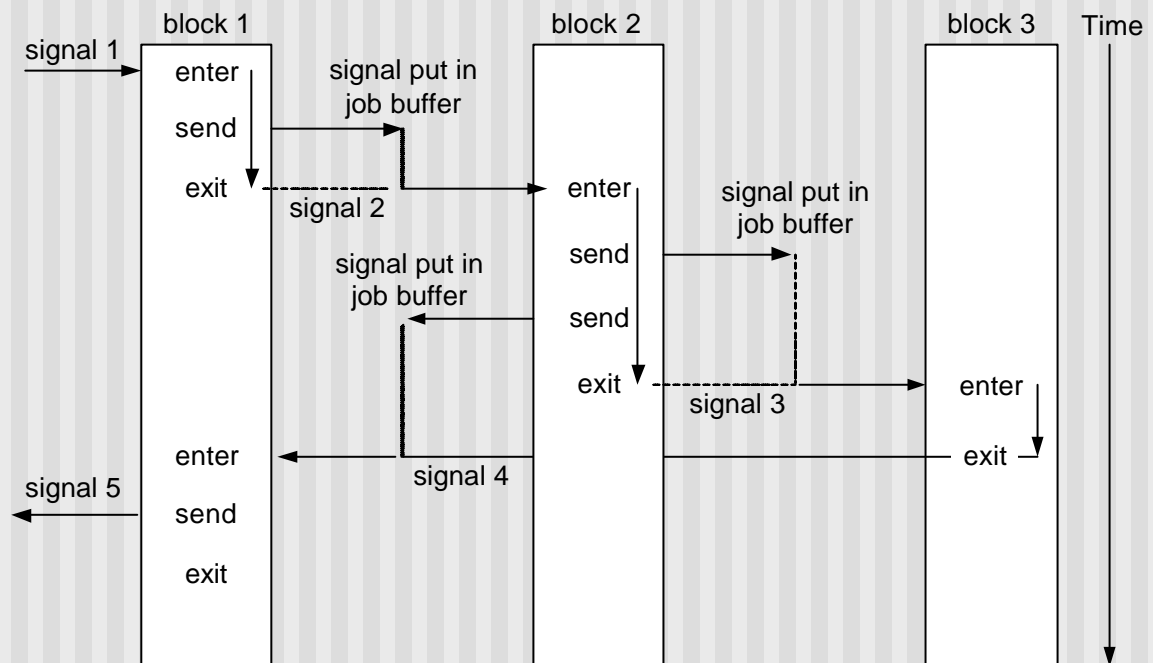
Single vs. Combined



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PLEX characteristics – execution (buffered signals)

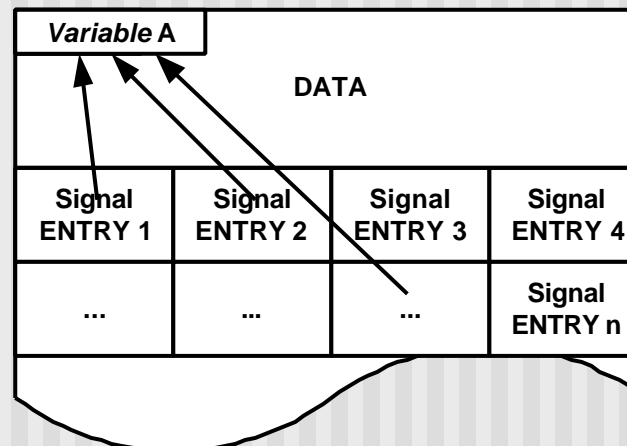
```
PROGRAM; PLEX;  
  ENTER SIGNAL1; ┌  
  .... ├ a subprogram  
  SEND BUFFERED SIGNAL2; └  
  ....  
  EXIT; ┌  
  
  ENTER SIGNAL3; ┌ a subprogram  
  .... ├  
  SEND DIRECT SIGNAL4; └  
  
  ENTER SIGNAL5; ┌ a subprogram  
  .... ├  
  SEND BUFFERED SIGNAL6; └  
  ....  
  SEND DIRECT SIGNAL7; ┌  
  
  ENTER SIGNAL8; ┌ a subprogram  
  .... ├  
  EXIT; └  
  
  ....  
END PROGRAM;
```



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Problem description

- Parallel execution - Unintentional interaction
- ~ 10 mil. codelines => Can't rewrite the system
- Implementation update - No exact documentation



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Motivation

- Ericsson wants to introduce parallel processing
- Exact documentation when updating the implementation

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An Operational semantics for PLEX

- Execution of statements modeled by state transitions
- $s = \langle VSC, RM, PS, DS, RS, JBA, JBB, JBC, JBD, JBR \rangle$
- *VSC* – Handle unstructured jumps (e.g. GOTO)
- $\langle \mathbf{GOTO} \textit{label}, s \rangle \Rightarrow s[VSC \mapsto \text{ADR}\{\textit{label}\}]$
- $\langle \mathbf{SEND} \textit{signal}, s \rangle \Rightarrow s[VSC ++, JBA:\textit{signal}]$

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Future work

- Verify that the formalism corresponds with actual execution
- Formal semantics for parallel PLEX